

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroyuki Nakajima

Examiner: Brian P. Johnson

Serial No.: 10/785,671

Art Unit: 2183

Filed: February 24, 2004

Docket: 17472

For: PROCESSOR, SYSTEM LSI
CIRCUIT, METHOD OF DESIGNING
SAME, AND RECORDING MEDIUM
HAVING SAID METHOD
RECORDED THEREON

Dated: May 21, 2007

Confirmation No.: 2687

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 23313-1450

AMENDMENT AND RESPONSE UNDER 37 C.F.R. §1.111

Sir:

The Applicant has filed this Amendment pursuant to 37 C.F.R. § 1.111 in reply to the outstanding Official Action dated February 20, 2007, and the Applicant believes the Amendment to be fully responsive to the Official Action for reasons set forth below.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on **May 21, 2007**.

Dated: May 21, 2007


Katherine R. Vieyra